

WHAT IS CLAIMED IS:

1. A laminated capacitor comprising:

a capacitor body having two opposite main surfaces, and two opposite side surfaces connected to the main surfaces and extending in a longer-side direction of the main surfaces, and two opposite end surfaces connected to the main surfaces and extending in a shorter-side direction of the main surfaces; wherein

the capacitor body includes a plurality of dielectric layers extending in the direction of the main surfaces, and at least a pair of first and second internal electrodes facing each other across a specific dielectric layer of the dielectric layers so as to define a capacitor unit;

the first and the second internal electrodes have first and second lead electrodes, respectively, extending onto the side surfaces and the end surfaces of the capacitor body;

first external terminal electrodes electrically connected to the first internal electrodes via the first lead electrodes and second external terminal electrodes electrically connected to the second internal electrodes via the second lead electrodes are respectively disposed on the side surfaces and the end surfaces of the capacitor body;

at least two of the first external terminal electrodes are disposed on each of the side surfaces and at least one of the first external terminal electrodes is disposed on each of the end surfaces;

at least two of the second external terminal electrodes are disposed on each of the side surfaces so as to be arranged alternately with the first external terminal electrodes, and at least one of the second external terminal electrodes is disposed on each of the end surfaces so as to be arranged alternately with the first external terminal electrode;

the number of the first and the second external terminal electrodes disposed on the side surfaces is larger than the number of the first and the second external terminal electrodes disposed on the end surfaces; and

an end-surface pitch which defines an interval between adjacent first and second external terminal electrodes disposed on the end surfaces is equal to or less than about 0.9 times of a side-surface pitch which defines an interval between adjacent first and second external terminal electrodes disposed on the side surfaces.

2. A laminated capacitor according to Claim 1, wherein an end-surface pitch which defines an interval between adjacent ones of the first and second lead electrodes electrically connected to the first and the second external terminal electrodes disposed on the end surfaces is equal to or less than about 0.9 times of a side-surface pitch which defines an interval between adjacent ones of the first and second lead electrodes electrically connected to the first and the second external terminal electrodes disposed on the side surfaces.

3. A laminated capacitor according to Claim 1, wherein the capacitor body is substantially rectangular.

4. A laminated capacitor according to Claim 1, wherein all of the first external terminal electrodes and all of the second external terminal electrodes are disposed alternately along the two side surfaces and the two end surfaces.

5. A laminated capacitor according to Claim 1, wherein the laminated capacitor is arranged to define a decoupling capacitor connected to a power-supply circuit for an MPU chip provided in a micro-processing unit.

6. A printed circuit board comprising a laminated capacitor according to Claim 1 mounted on a surface of the printed circuit board.

7. A printed circuit board according to Claim 6, further comprising an MPU chip provided for a micro-processing unit mounted on the surface of the printed circuit board.

8. A decoupling circuit comprising a laminated capacitor according to Claim 1.

9. A high-frequency circuit comprising a laminated capacitor according to Claim 1.

10. A laminated capacitor comprising:

a capacitor body having two opposite main surfaces, and two opposite side surfaces connecting the main surfaces and extending in a longer-side direction of the main surfaces, and two opposite end surfaces connecting the main surfaces and extending in a shorter-side direction of the main surfaces;

wherein the capacitor body includes a plurality of dielectric layers extending in the direction of the main surfaces, and at least a pair of first and second internal electrodes facing each other across a specific dielectric layer of the dielectric layers so as to define a capacitor unit;

the first and the second internal electrodes have first and second lead electrodes, respectively, extending onto the side surfaces and the end surfaces of the capacitor body;

first external terminal electrodes electrically connected to the first internal electrodes through the first lead electrodes and second external terminal electrodes electrically connected to the second internal electrodes through the second lead electrodes are disposed on the side surfaces and the end surfaces of the capacitor body;

at least two of the first external terminal electrodes are disposed on each of the side surfaces and at least one of the first external terminal electrodes is disposed on each of the end surfaces;

at least two of the second external terminal electrodes are disposed on each of the side surfaces so as to be arranged alternately with the first external terminal electrodes and at least one of the second external terminal electrodes is disposed on each of the end surfaces so as to be arranged alternately with the first external terminal electrode;

the number of the first and the second external terminal electrodes disposed on the side surfaces is larger than the number of the first and the second external terminal electrodes disposed on the end surfaces; and

an end-surface pitch which defines an interval between adjacent first and second lead electrodes electrically connected to the first and the second external terminal electrodes disposed on the end surfaces is equal to or less than about 0.9 times of a side-surface pitch which defines an interval between adjacent first and second lead electrodes electrically connected to the first and the second external terminal electrodes disposed on the side surfaces.

11. A laminated capacitor according to Claim 10, wherein an end-surface pitch which defines an interval between adjacent first and second external terminal electrodes disposed on the end surfaces is equal to or less than about 0.9 times of a side-surface pitch which defines an interval between adjacent first and second external terminal electrodes disposed on the side surfaces.

12. A laminated capacitor according to Claim 10, wherein the capacitor body is substantially rectangular.

13. A laminated capacitor according to Claim 10, wherein all of the first external terminal electrodes and all of the second external terminal electrodes are disposed alternately along the two side surfaces and the two end surfaces.

14. A laminated capacitor according to Claim 10, wherein the laminated capacitor is arranged to define a decoupling capacitor connected to a power-supply circuit for an MPU chip provided in a micro-processing unit.

15. A printed circuit board comprising a laminated capacitor according to Claim 10 mounted on a surface of the printed circuit board.
16. A printed circuit board according to Claim 15, further comprising an MPU chip provided for a micro-processing unit mounted on the surface of the printed circuit board.
17. A decoupling circuit comprising a laminated capacitor according to Claim 10.
18. A high-frequency circuit comprising a laminated capacitor according to Claim 10.